

IN THE CLAIMS

Please amend the claims as follows:

Claims 1-8 (Canceled).

Claim 9 (Currently Amended): An amplifier comprising:

an ~~amplifier~~ amplification device; [[and]]

an LC parallel resonant circuit connected in parallel to the amplification device; and

an LCR series resonant circuit provided connected in parallel to the amplification

device and the LC parallel resonant circuit as a load for the amplifier device.

Claim 10 (Previously Presented): An amplifier according to claim 9, wherein a common-gate circuit and a cascade circuit are combined.

Claim 11 (Previously Presented): An amplifier according to claim 9, wherein a common-source circuit, a cascade circuit, and a voltage feedback circuit are combined.

Claim 12 (Currently Amended): A wireless communication apparatus comprising:

an antenna; [[,]]

a band-pass filter; [[,]]

a low noise amplifier configured to amplify which amplifies a voltage of a received signal [[,]] and including

an amplification device,

an LC parallel resonant circuit connected in parallel to the amplification  
device, and

an LCR series resonant circuit connected in parallel to the amplification device and the LC parallel resonant circuit;  
a down-converter configured to down-convert which down-converts the voltage-amplified received signal by frequency conversion; [[,]]  
an automatic gain controller; [[,]]  
an analog-digital converter; [[,]] and  
a signal processing circuit configured to perform which performs digital signal processing of received data, ~~wherein the low noise amplifier is an amplifier according to claim 9.~~

Claim 13 (Currently Amended): A wireless communication apparatus comprising:  
an antenna; [[,]]  
a band-pass filter; [[,]]  
a low noise amplifier configured to amplify which amplifies a voltage of a received signal [[,]] and including  
an amplification device,  
an LC parallel resonant circuit connected in parallel to the amplification device, and  
an LCR series resonant circuit connected in parallel to the amplification device and the LC parallel resonant circuit;  
a down-converter configured to down-convert which down-converts the voltage-amplified received signal by frequency conversion; [[,]]  
an automatic gain controller; [[,]]  
an analog-digital converter; [[,]]

a digital-analog converter configured to convert which converts transmit data to an analog signal; [,.]

an up-converter configured to up-convert which up-converts the analog transmit signal by frequency conversion; [,.]

a power amplifier configured to amplify a which amplifies power of the up-converted transmit signal; [,.] and

a signal processing circuit configured to perform which performs digital signal processing of transmit/receive data, ~~wherein the low noise amplifier is an amplifier according to claim 9.~~

Claim 14 (Currently Amended): An amplifier comprising:

an amplifier amplification device; and  
a band-pass filter connected to an output terminal of ~~provided as a load for the~~  
~~amplifier amplification~~ device, the band-pass filter [[and]] having an s-plane in which a plurality of poles [[are]] provided on a left side of an s-plane and a plurality of zeros are provided arranged between the poles, at least two zeros being arranged at locations other than an origin of the s-plane.

Claim 15 (Previously Presented): An amplifier according to claim 14, wherein the band-pass filter does not have a capacitor provided in series with an output terminal of the amplifier.

Claim 16 (Currently Amended): An amplifier according to claim 14, wherein an inductance and a capacitor are not provided in series between an output terminal of the amplifier amplification device and an output terminal of the amplifier.

Claim 17 (Previously Presented): An amplifier according to claim 14, wherein a common-gate circuit and a cascade circuit are combined.

Claim 18 (Previously Presented): An amplifier according to claim 14, wherein a common-source circuit, a cascade circuit, and a voltage feedback circuit are combined.

Claim 19 (Currently Amended): A wireless communication apparatus comprising:

an antenna; [,,]

a band-pass filter; [,,]

a low noise amplifier configured to amplify which amplifies a voltage of a received signal [,,] and including

an amplification device, and

a band-pass filter connected to an output terminal of the amplification device,

the band-pass filter having a plurality of poles provided on a left side of an s-plane

and a plurality of zeros arranged between the poles, at least two zeros being arranged

at locations other than an origin of the s-plane;

a down-converter configured to down-convert which down-converts the voltage-amplified received signal by frequency conversion; [,,]

an automatic gain controller; [,,]

an analog-digital converter; [,,] and

a signal processing circuit configured to perform which performs digital signal processing of received data, ~~wherein the low noise amplifier is an amplifier according to~~

~~claim 14.~~

Claim 20 (Currently Amended): A wireless communication apparatus comprising:

an antenna; [ , ]

a band-pass filter; [ , ]

a low noise amplifier configured to amplify which amplifies a voltage of a received signal [ , ] and including

an amplification device, and

a band-pass filter connected to an output terminal of the amplification device,

the band-pass filter having a plurality of poles provided on a left side of an s-plane

and a plurality of zeros arranged between the poles, at least two zeros being arranged

at locations other than an origin of the s-plane;

a down-converter configured to down-convert which down-converts the voltage-amplified received signal by frequency conversion; [ , ]

an automatic gain controller; [ , ]

an analog-digital converter; [ , ]

a digital-analog converter configured to convert which converts transmit data to an analog signal; [ , ]

an up-converter configured to up-convert which up-converts the analog transmit signal by frequency conversion; [ , ] a power amplifier configured to amplify a which

amplifies power of the up-converted transmit signal; [ , ] and

a signal processing circuit configured to perform which performs digital signal processing of transmit/receive data, ~~wherein the low noise amplifier is an amplifier according to claim 14.~~